

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electronic circuit, comprising:
 - a capacitor that stores a current signal supplied to the capacitor during a first period and a voltage signal supplied to the capacitor during a second period; and
 - a first transistor that includes a first gate, a first drain and a first source,
 - a conduction state of the first transistor being set in accordance with a charge stored by the capacitor,
 - a first current as the current signal flowing through the first transistor during at least a part of the first period, and
 - no current flowing through the first transistor during ~~at least a part of the~~ second period.
2. (Previously Presented) The electronic circuit according to Claim 1, further comprising:
 - a second transistor,
 - the current signal and the voltage signal being supplied to the capacitor through the second transistor.
3. (Previously Presented) The electronic circuit according to Claim 1, further comprising:
 - a third transistor that controls an electrical connection between the first gate and the first drain.
4. (Previously Presented) The electronic circuit according to Claim 1, further comprising:

a fourth transistor that controls a timing to start or stop supply of the current to the electronic element after the conduction state of the first transistor is set according to at least one of the current signal and the voltage signal.

5. (Previously Presented) The electronic circuit according to Claim 1, further comprising:

a fifth transistor, the amount of charge held in the capacitor being reset to a predetermined state when the fifth transistor is turned on.

6. (Currently Amended) An electro-optical device, comprising:

a plurality of scanning lines;

a plurality of data lines;

~~a plurality of electro-optical elements that are disposed corresponding to intersections between the plurality of scanning lines and the plurality of data lines;~~

~~_____ a first electrode that is disposed opposite to a plurality of second electrodes, each of which is included in one electro-optical element of the plurality of electro-optical elements; a plurality of unit circuits that include a plurality of electro-optical elements;~~

a first circuit that outputs a current signal that is accumulated in a capacitor included in each of the plurality of unit ~~circuits, circuits;~~ ~~each of which includes one electro-optical element of the plurality of electro-optical elements;~~ and

a second circuit that outputs a voltage signal that is accumulated in a capacitor in the each of the plurality of unit ~~circuits, circuits;~~ and

_____ a first electrode that is disposed opposite to a plurality of second electrodes, each of which is included in one electro-optical element of the plurality of electro-optical elements included in the plurality of unit circuits,

no current flowing through a transistor included in one unit circuit of the plurality of unit circuits during a second period in which the voltage signal is supplied to the one unit circuit.

7. (Previously Presented) The electro-optical device according to Claim 6, the current signal and voltage signal being supplied to each of the plurality of unit circuits through one data line of the plurality of data lines.

8. (Previously Presented) The electro-optical device according to Claim 6, the plurality of data lines including a plurality of first data lines and a plurality of second data lines,

the current signal being supplied to each of the plurality of unit circuits through one first data line of the plurality of first data lines; and

the voltage signal being supplied to each of the plurality of unit circuits through one second data line of the plurality of second data lines.

9-12. (Canceled)

13. (Previously Presented) The electro-optical device according to Claim 22, the electro-optical element being an EL element.

14. (Previously Presented) The electro-optical device according to Claim 13, the EL element including a light-emitting layer that is composed of an organic material.

15-19. (Canceled)

20. (Previously Presented) An electronic apparatus, comprising:
the electro-optical device according to Claim 6.

21. (Previously Presented) The electronic circuit according to Claim 1,
the current signal being a multi-valued data current, and
the voltage signal being a binary data voltage.

22. (Previously Presented) The electro-optical device according to Claim 6,
each of the plurality of unit circuits including an electro-optical element.

23. (Currently Amended) An electronic circuit, comprising:

a capacitor that accumulates a current signal that is received by the electronic circuit during a first period, the capacitor accumulating a voltage signal that is received by the electronic circuit during a second period; and

a first transistor whose conduction state is set in accordance with an amount of charge accumulated in the capacitor stored during a selected period from the first period and the second period, the first transistor including a first gate, a first drain and a first source, the first transistor supplying a current whose amount is determined in accordance with the conduction state to an electronic ~~element~~element,

no current flowing through the first transistor during the second period.

24. (Currently Amended) An electronic circuit, comprising:

a capacitor that accumulates a current signal that is received by the electronic circuit in a first mode, the capacitor accumulating a voltage signal that is received by the electronic circuit in a second mode; and

a first transistor whose conduction state is set in accordance with an amount of charge accumulated in the capacitor stored during a selected mode from the first mode and the second mode, the first transistor including a first gate, a first drain and a first source, the first transistor supplying a current whose amount is determined in accordance with the conduction state to an electronic ~~element~~element,

no current flowing through the first transistor during the second mode.

25. (Currently Amended) The electronic circuit according to Claim 23,

the current signal corresponding to analog data current, and

the voltage signal corresponding to digital data voltage.

26. (Currently Amended) The electronic circuit according to Claim 24,
the current signal corresponding to analog data current, and
the voltage signal corresponding to digital data voltage.
27. (Previously Presented) The electronic circuit according to Claim 24,
a power consumption in the second mode being lower than a power
consumption in the first mode.
28. (Previously Presented) The electronic circuit according to Claim 23, further
comprising:
a second transistor,
the current signal and the voltage signal being supplied to capacitor through
the second transistor.
29. (Previously Presented) The electronic circuit according to Claim 23, further
comprising:
a third transistor that controls an electrical connection between the first gate
and the first drain.
30. (Previously Presented) The electronic circuit according to Claim 1, further
comprising:
an electronic element,
a second current whose current level corresponds to the conduction state of the
first transistor being supplied to the electronic element.
31. (Previously Presented) The electro-optical device according to Claim 6,
a potential of the first electrode being set at a constant during at least a part of
a first period in which the current signal is supplied to the capacitor, and
the potential of the first electrode being set at the constant during at least a part
of a second period in which the voltage signal is supplied to the capacitor.

32. (New) The electronic circuit according to Claim 1,
the current signal corresponding to analog data current, and
the voltage signal corresponding to digital data voltage.
33. (New) The electro-optical device according to Claim 6,
the current signal corresponding to analog data current, and
the voltage signal corresponding to digital data voltage.